

4.2 Input Queueing

(a) the Single-Queue per Input Case

- Crossbar Switch with one Buffer Memory per Input Line
- Throughput per Buffer Memory = 1 (incoming) + 1 (outgoing)
- Not the Dual of Output Queueing
 - Output-queueing throughput per buffer memory = $N+1$
 - Output queueing does *not* contain a crossbar:
a crossbar allows each output to be fed by at most *one* input;
output queueing allows each output to be fed by *any* (even all) inputs
- Crossbar Scheduling is an issue
 - Single queue per input \Rightarrow independent schedulers per-output (easy)
 - Multiple queues per input (next section) \Rightarrow difficult...

Input Queueing Family

- Buf. Mem. throughput = const = 2 (each)

total for all = $2N$ = min. possible

- How many Queues per Inp. Buf. Mem.?

- single queue: "input queueing"

... Head-of-Line Blocking

→ bad performance (see later)

- many queues (per output or more):

"advanced input queueing" or "input buffering"
or "virtual output queueing"

- What Switching Fabric?

- crossbar ... up to $N \sim$ hundred(s)

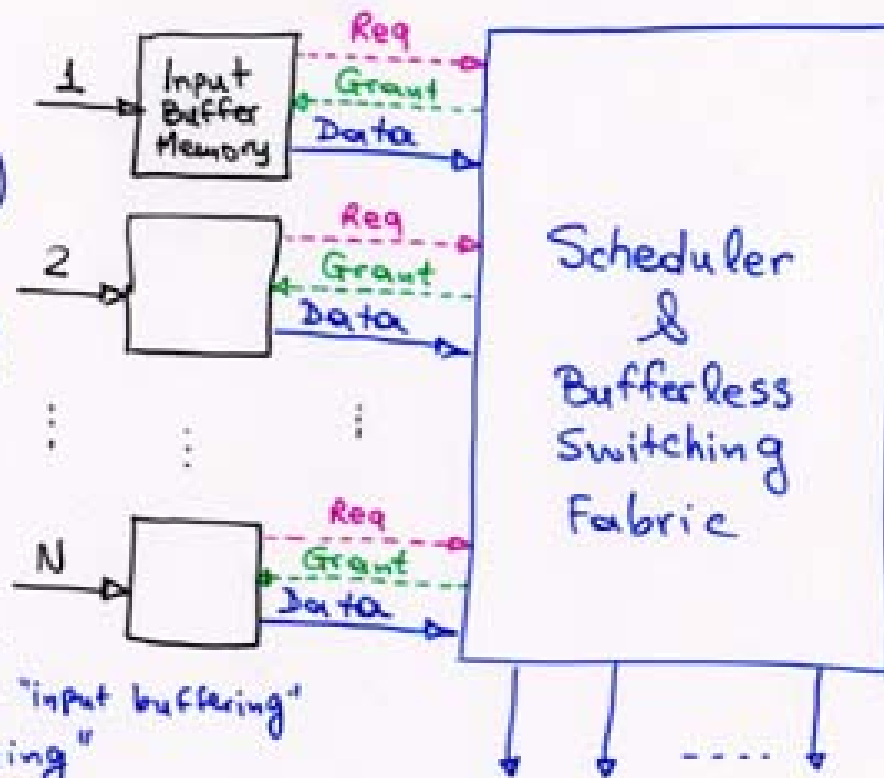
- multistage ... see chapter 5

- What Scheduler? ... What Performance?

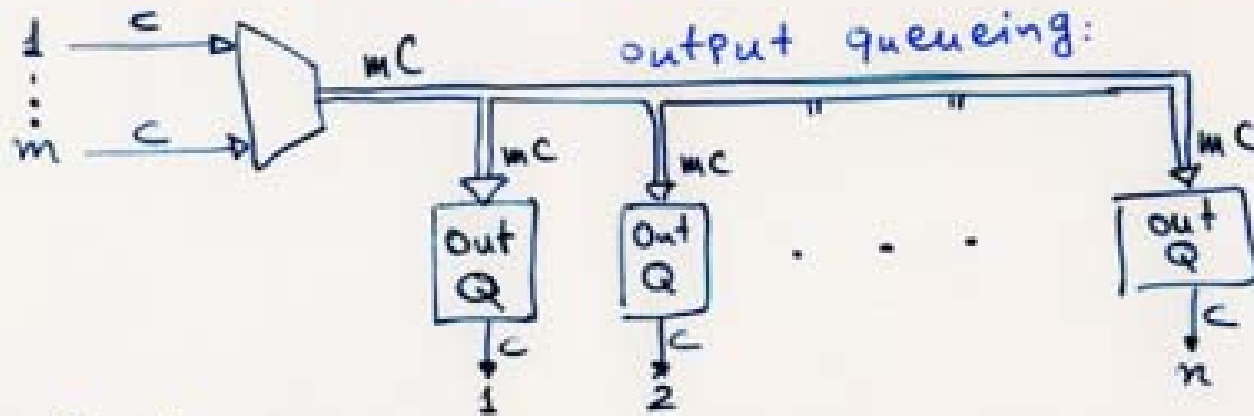
- difficult

- non scalable

- big research topic for last >10 years and still ... See reading list papers

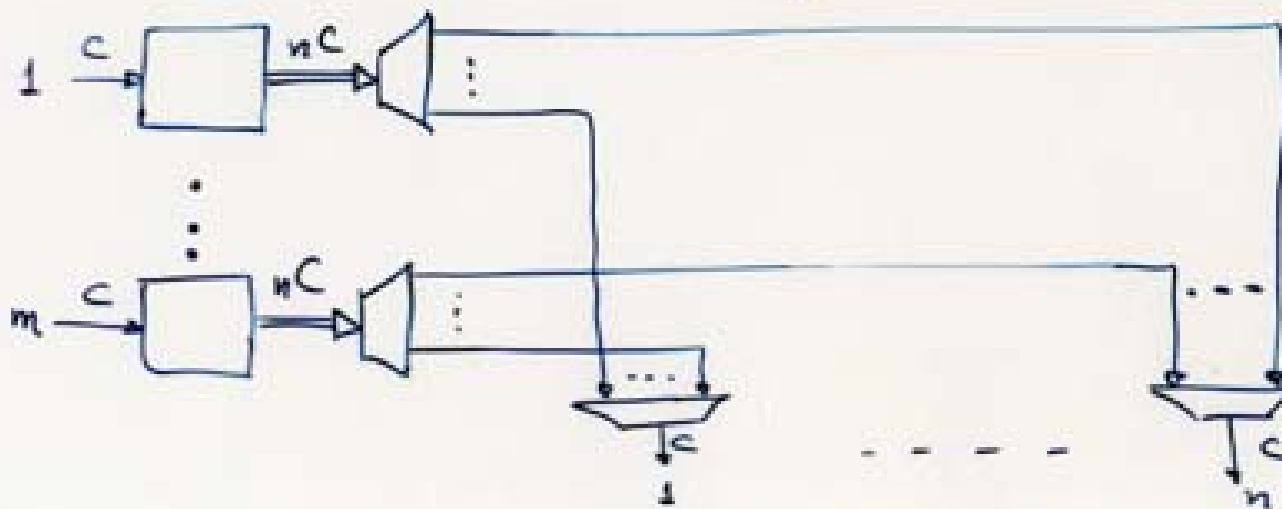


Note: Input Queueing is NOT the dual of Output Queueing!



the dual would be:

(like "internal speed-up" of n, but without the output buffers)



Old-Fashioned Input Queueing: Single Queue per Input

Head-of-Line (HDL) Blocking

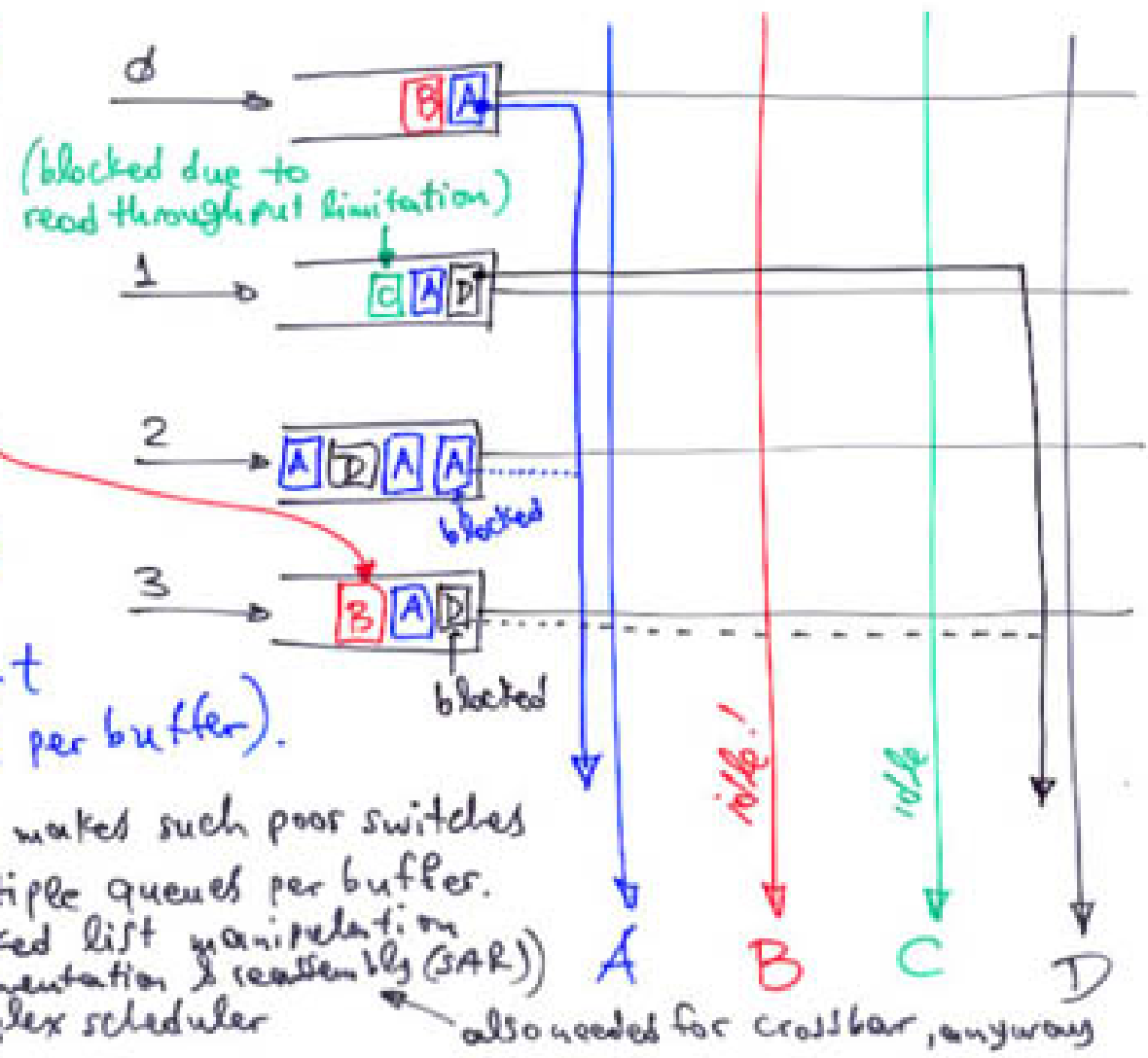
Although output B is idle and input buffer 3 is also idle (at least on the read port), this packet cannot depart due to inadequate data structure support (single logical queue per buffer).

• After the 90's, nobody makes such poor switches anymore. Solution: multiple queues per buffer.

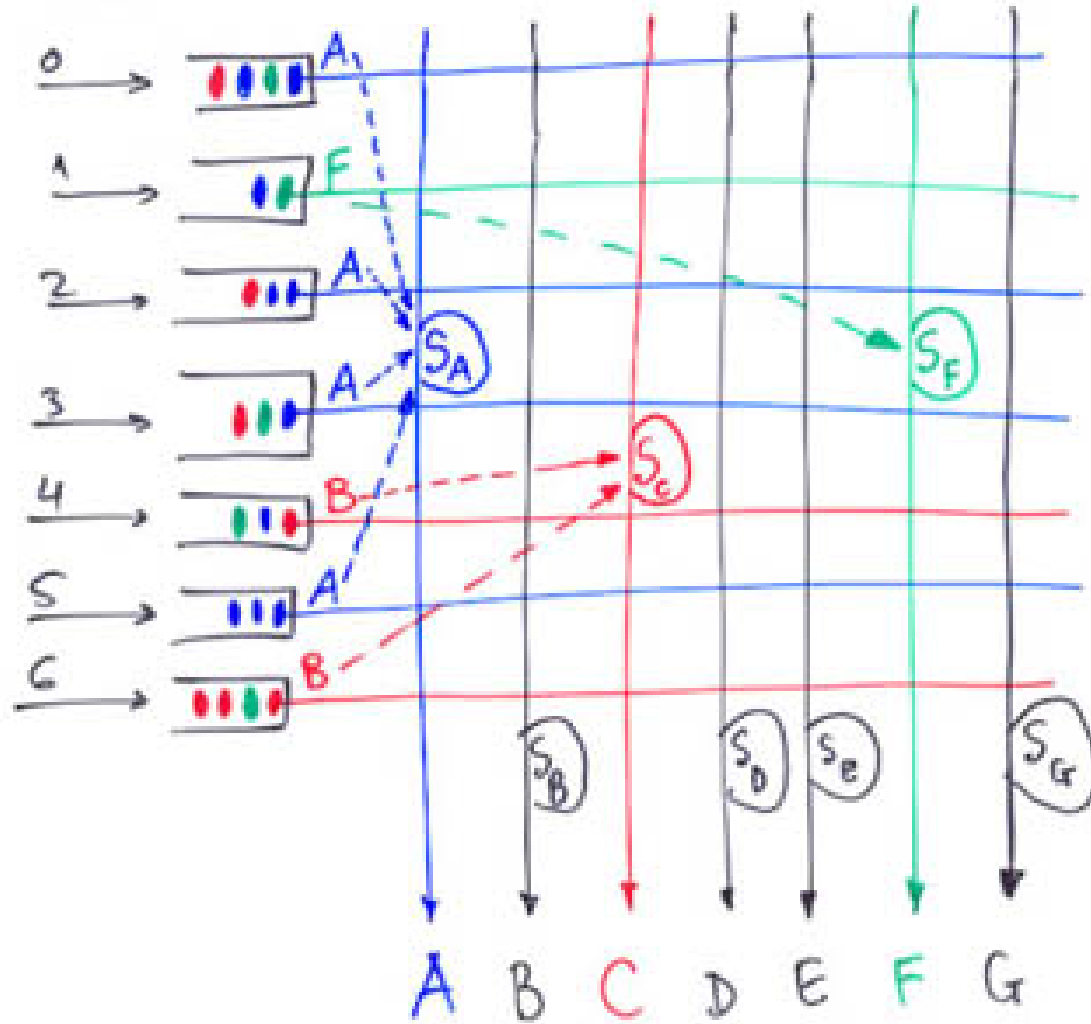
Cost of Solution:

- linked list manipulation
- segmentation & reassembly (SAR)
- complex scheduler

also needed for crossbar, anyway



Independent Per-Output Schedulers

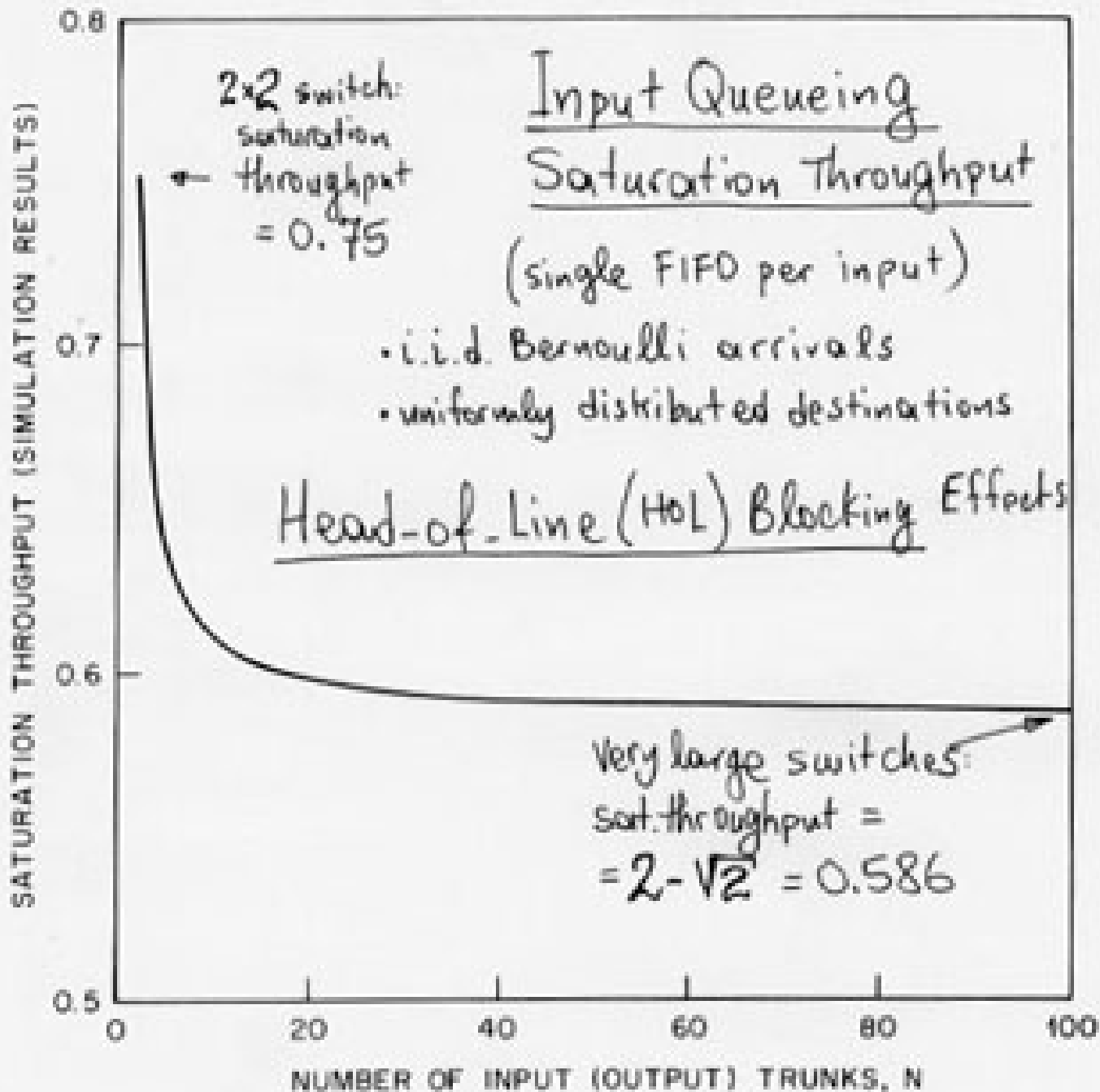


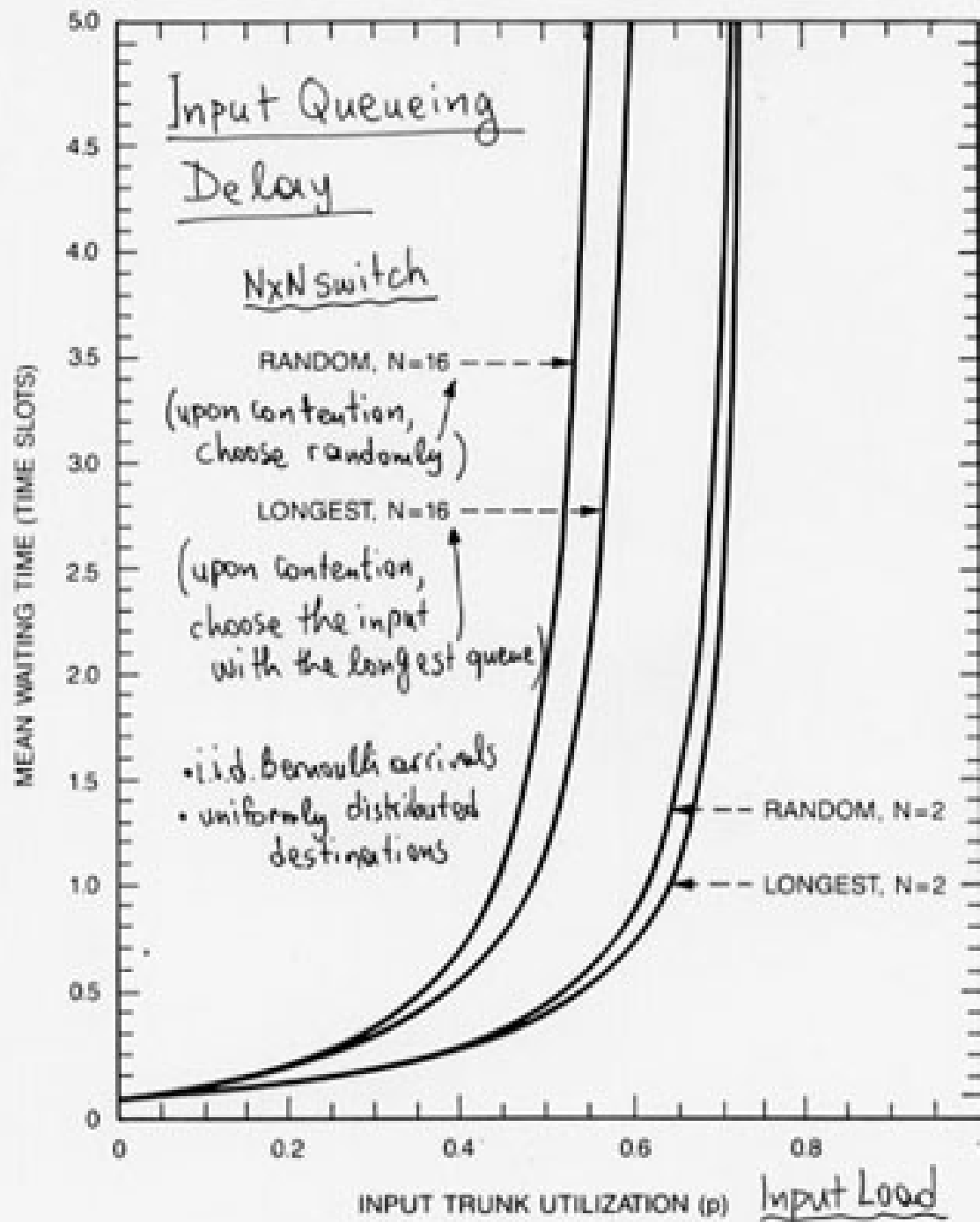
- with old-fashioned input Q.
 - (as with crosspoint queuing (similar to output Q or shared buffer)).

- each output has its own scheduler
 - each scheduler sees a distinct set of requests
 - each scheduler independently makes a decision
 - no better decision is possible by considering the other schedulers' decisions.
- ... unlike the next scheme (adv. int. buffering - VOQ's) ...

Throughput & Delay under Single-Queue Input Q'ing

- *References:*
 - M. Hluchyj, M. Karol: “Queueing in High-Performance Packet Switching”, IEEE Journal on Sel. Areas in Commun. (JSAC), Dec. 1988, pp. 1587-1597.
 - M. Karol, M. Hluchyj, S. Morgan: “Input versus Output Queueing on a Space-Division Packet Switch”, IEEE Tr. on Communic., Dec. 1987, pp. 1347-1356.
 - J. Hui, E. Arthurs: “A Broadband Packet Switch for Integrated Transport”, IEEE Journal on Sel. Areas in Commun. (JSAC), Oct. 1987, pp. 1264-1273.
- Attention: results for i.i.d. Bernoulli (non-bursty) arrivals, with uniformly-distributed destinations (no overloaded hot-spots), are only useful for gaining a rough, first insight into the behavior of systems, but are often not representative of the real behavior of systems under real traffic!...





Output Queueing Delay

- i.i.d. Bernoulli arrivals
- uniformly distributed destinations

