CS425: Computer Systems Architecture

Homework Problem Set 4

Assignment: December 3, 2012

Due: December 10, 2012 – 23:59:59

Instructions: Solve all problems in a **.pdf** file and send them via e-mail to Vassilis Papaefstathiou (papaef@csd.uoc.gr). Use the subject: **HY425 - Homework 4**

Consider the following C code:

```
for ( i=1 ; i<=N ; i++ ){
    A[i] = A[i] + B[i];
    B[i] = x * B[i];
}
```

Use the techniques discussed during the lectures on vector processors to estimate the performance of a vector processor throughout this exercise, assuming you have a 1-GHz version of VMIPS.

Problem 1 (16 points)
Write the best VMIPS vector code for the inner portion of the loop.
Assume x is in F0 and the addresses of A and B are in Ra and Rb, respectively.

Problem 2 (15 points)
Find the total time for this loop on VMIPS for n=1000 (T_{1000}).
What is the MFLOPS rating for the loop (R_{1000})?

Problem 3 (12 points)
Find $R\infty$ for this loop.

Problem 4 (12 points)
Find $N_{1/2}$ for this loop.

Problem 5 (15 points)
Find N_v for this loop. Assume the scalar code has been pipeline scheduled so that each memory reference takes six cycles and each FP operation takes three cycles. Assume the scalar overhead is also T_{loop}.

Problem 6 (15 points)
Assume VMIPS has two memory pipelines. Write vector code that takes advantage of the second memory pipeline. Show the layout in convoys.

Problem 7 (15 points)
Compute T_{1000} and R_{1000} for VMIPS with two memory pipelines.